

Claims

- [c1] What is claimed is:
- 1.A polysilicon thin film transistor liquid crystal display comprising:
 - a panel;
 - a common voltage layer formed in the panel;
 - a plurality of display cells;
 - a plurality of scan lines formed in the panel and coupled to the display cells;
 - a plurality of data lines formed in the panel and coupled to the display cells; and
 - a plurality of common voltage drivers formed in the panel, each common voltage driver being for generating a common voltage applied to the common voltage layer.
 - [c2] 2.The polysilicon thin film transistor liquid crystal display of claim 1 wherein the common voltage drivers comprise polysilicon thin film transistors.
 - [c3] 3.The polysilicon thin film transistor liquid crystal display of claim 1 wherein the common voltage is an alternating voltage.
 - [c4] 4.The polysilicon thin film transistor liquid crystal dis-

play of claim 1 further comprising:
a scan line driver coupled to the plurality of scan lines;
at least a data line driver coupled to the plurality of data lines; and
a timing control circuit for generating a timing signal;
wherein the scan line driver and the data line driver control operations of the display cells based on the timing signal.

[c5] 5.The polysilicon thin film transistor liquid crystal display of claim 4 wherein the common voltage drivers, the scan line driver, the data line driver, and the timing control circuit comprise polysilicon thin film transistors.

[c6] 6.The polysilicon thin film transistor liquid crystal display of claim 1 further comprising an interface for receiving and transmitting an image signal such that the display cells operate based on the image signal.

[c7] 7.The polysilicon thin film transistor liquid crystal display of claim 1 wherein each display cell further comprises:

a liquid crystal component comprising:

a pixel electrode; and

a common electrode coupled to the common voltage layer; and

a polysilicon thin film transistor comprising:

a gate electrically connected to a corresponding scan line;

a source electrically connected to a corresponding data line; and

a drain electrically connected to the pixel electrode of the liquid crystal component.